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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)		Application Number	10/685,518
		Filing Date	October 16, 2003
		First Named Inventor	Chul-Sung KIM, et al
		Art Unit	2824
		Examiner Name	C. WILSON
Sheet 2	of 2	Attorney Docket Number	239/162 DIV.2

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
cdw		WOLF, et al., Silicon Processing for the VLSI Era, Vol 1, Process Technology, Lattice Press, p.201	
		LO, et al, MOS Characteristics of Fluorinated ..., IEEE Electron Device Ltrs, 11(11):511-513, (1990)	
		WRIGHT, et al., Hot-Electron Immunity of SiO <sub>2</sub> Dielectrics with Fluorine., IEEE, pp. 347-348, (1989)	
cdw		HOOK, et al., Correlation Between Fixed Positive Charge and Hot-Electron., IEEE, pp. 471-473, (1997)	

Examiner Signature	Christa Wilson	Date Considered	5/18/04
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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